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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Charles P. Roth et al. Art Unit : 2183
Serial No. : 09/704,467 Examiner : Aimee J. Li
Filed : October 31, 2000 Assignee : Intel Corporation
Title : EFFICIENT EMULATION INSTRUCTION DISPATCH BASED ON
INSTRUCTION WIDTH

RECEIVED

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DEC 19 2003

Technology Center 2100

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	28	-	23	=	5	\$90
Independent	4	-	4	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 0 month extension of time.

\$0

TOTAL FEE DUE

\$90

A check for \$90 is attached.

Respectfully submitted,


ALEX CHEN
REG NO 45,591

Date: December 8, 2003

Scott C. Harris
Reg. No. 32,030

Attorneys for Intel Corporation

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 8, 2003

Date of Deposit

Signature

Carroll Allman

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